

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

MELIK ISBARA

Application No.: 08/925,868

Filed: September 9, 1997

For: **AC COUPLED AND BOOTSTRAPPED
VOLTAGE-TOLERANT INPUT BUFFER
FOR INTEGRATED CIRCUITS -
UTILITY**

Art Group: 2816

Examiner: Kenneth B. Wells

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted on or before payment of the Issue Fee. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Copies of the references cited on PTO/SB/08 are enclosed herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

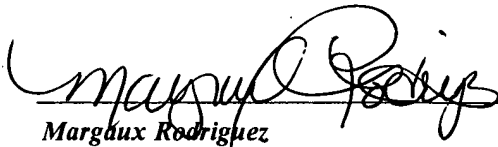
Date: February 19, 2004



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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Margaux Rodriguez

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Date

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	08/925,868
Filing Date	September 9, 1997
First Named Inventor	Melik Isbara
Art Unit	2816
Examiner Name	Kenneth B. Wells
Attorney Docket Number	42390P4537

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		David Greenhill, et al., "FA 10.2: A 330MHz 4-Way Superscalar Microprocessor", Digest of Technical Papers, ISSCC97/February 1997 IEEE International Solid-State Circuits Conference, Sun Microsystems Inc., Mountain View, CA (Pgs. 166-167)	

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.
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